Xyce[™] Parallel Electronic Simulator Release Notes

Beta Minor Release 2.1

Errata

This document spells out errata identified in the 0.2.1 (Beta 2.1) version of the **Xyce** Parallel Electronic. The version number 0.2.1 is used by the **Xyce** Release and Distribution Management documentation. Please see the other release documents (Beta 2 Release Notes and Beta 2.1 Addendum as well as the **Xyce** User's Guide) for a more complete description of **Xyce**.

This document is a list of errata discovered after the 0.2.1 release which will be fixed in the upcoming Version 1.0 release. Information found in this document includes:

- Any defects discovered since the last release.
- Any known workarounds to these defects.

For information pertaining to hardware requirements, software requirements, and known defects, see the Version 0.2 release notes.

For up-to-date information not available at the time these notes were produced, please visit the **Xyce** web page at http://www.cs.sandia.gov/xyce.

Known Problems

- Optional BJT node not handled correctly:
 - The optional "bulk" node for the BJT is not handled correctly, and specifying the optional node generates incorrect netlist connectivity.
 - Workaround: None. This has been fixed in the upcoming Release 1.0 version.
- Resistor device does not handle Celsius temperature input properly.
 - Resistor device does not convert temperature from Celsius to Kelvin and therefore gives erroneous results if a temperature is entered in Celsius on the instance line.
 - Workaround: Ensure that all resistor temperature parameters are entered in degrees Kelvin.
- No .PRINT line
 - If a user runs a netlist without a .PRINT line then **Xyce** core dumps (or on MS Windows 2000 gives a meaningless "Xyce.exe has generated errors and will be closed by Windows" error). This happens at the very end of the run.
 - Workaround: Ensure that all **Xyce** netlists have a .PRINT line to control results output.

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